

Method and system using hardware assistance
for continuance of trap mode during or after interruption sequences

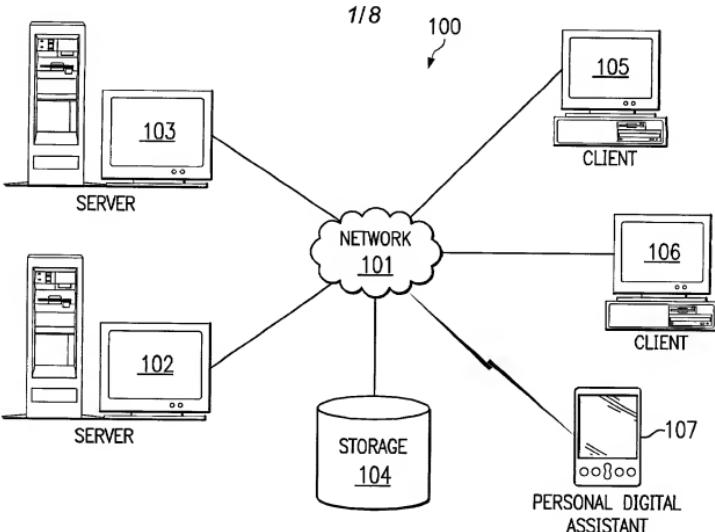


FIG. 1A
(PRIOR ART)

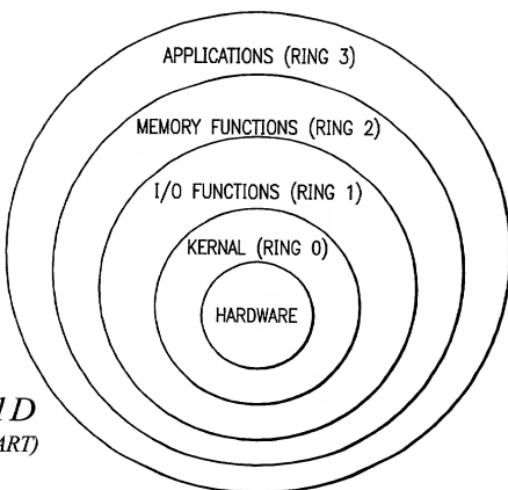


FIG. 1D
(PRIOR ART)

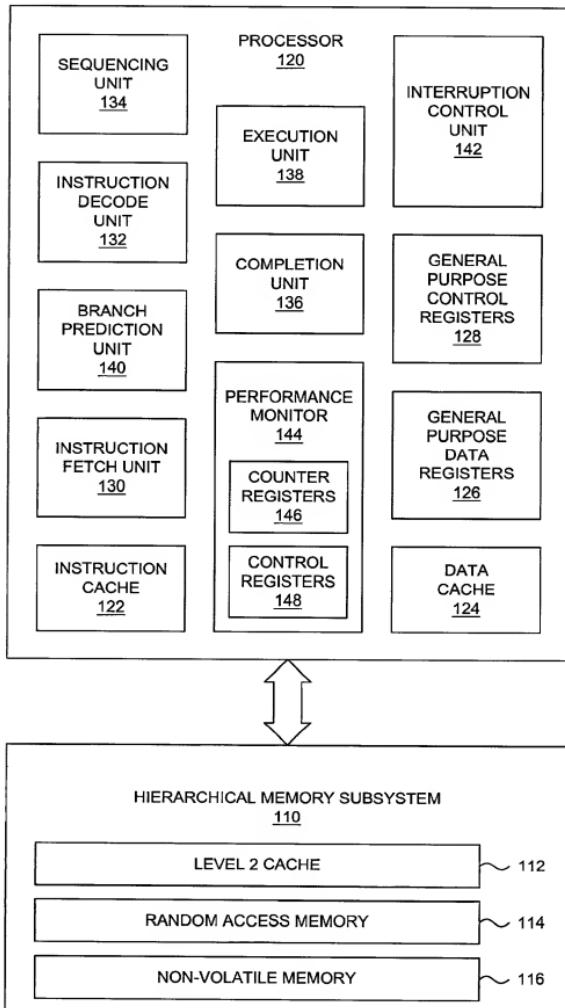


FIG. 1B
(PRIOR ART)

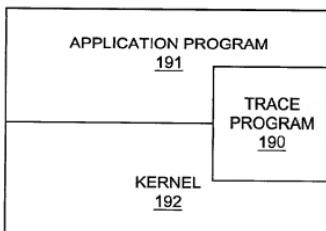


FIG. 1E
(PRIOR ART)

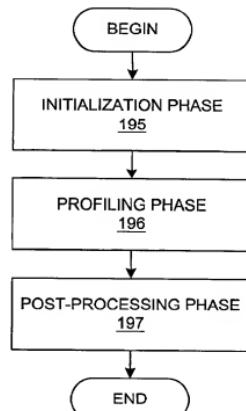


FIG. 1F
(PRIOR ART)

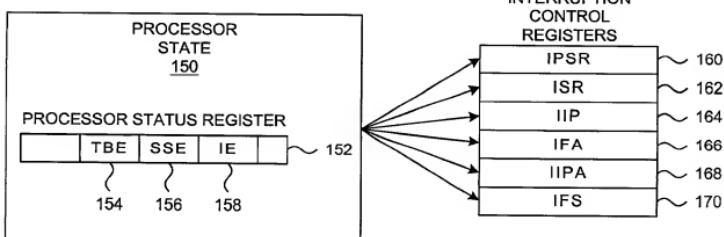


FIG. 1C
(PRIOR ART)

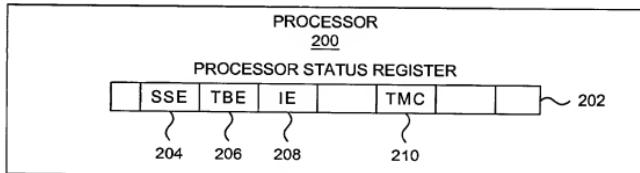


FIG. 2A

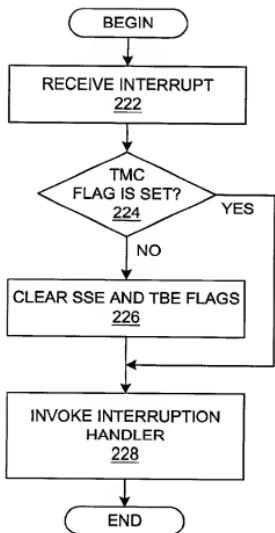


FIG. 2B

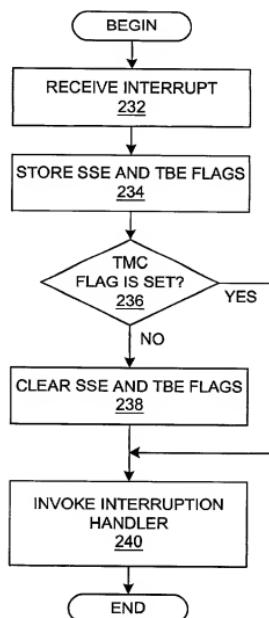


FIG. 2C

TRAP MODE CONDITIONING FIELD VALUES	
0 0 0	TRAP MODE NOT PRESERVED DURING OR AFTER INTERRUPTION
0 0 1	TRAP MODE RESTORED UPON EXECUTION OF RFI INSTRUCTION
1 0 0	TRAP MODE PRESERVED INTO INTERRUPTION HANDLER
1 0 1	TRAP MODE PRESERVED--TRAP MODE HANDLER ALLOWS INTERRUPTION HANDLER TO EXECUTE AT FULL SPEED--NO TRACE RECORD
1 1 0	TRAP MODE PRESERVED--TRAP MODE HANDLER ALLOWS INTERRUPTION HANDLER TO EXECUTE AT FULL SPEED--TRACE RECORD AT ENTRY
1 1 1	TRAP MODE PRESERVED--TRAP MODE HANDLER ALLOWS INTERRUPTION HANDLER TO EXECUTE AT FULL SPEED--TRACE RECORD AT EXIT

FIG. 2D

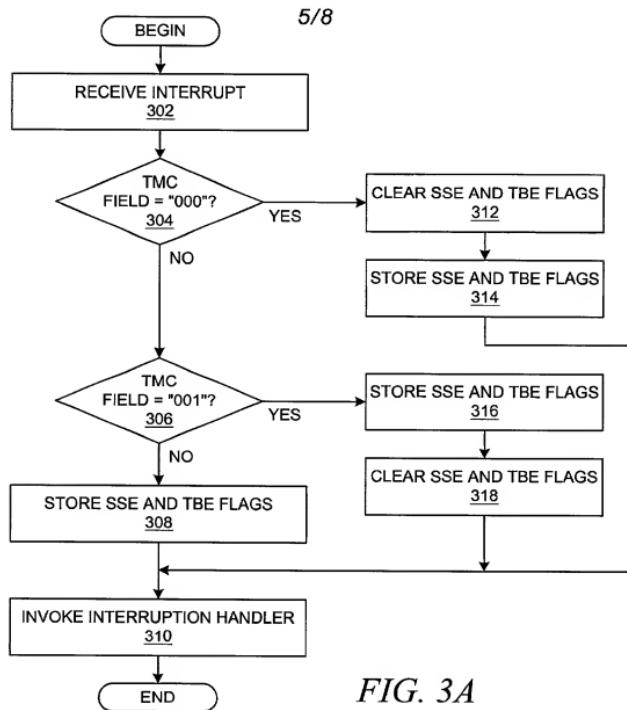


FIG. 3A

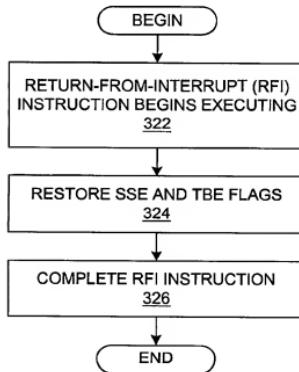


FIG. 3B

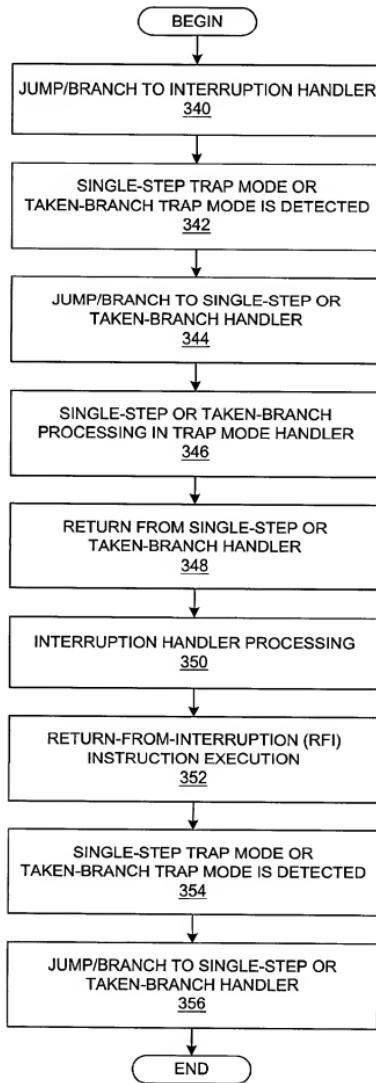


FIG. 3C

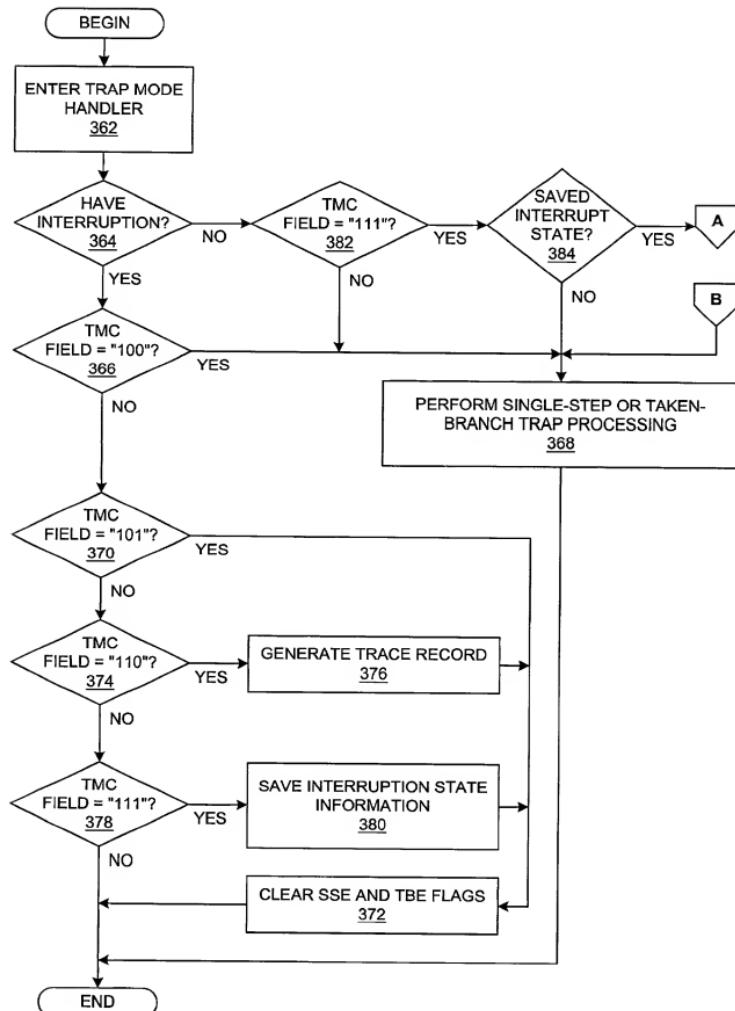


FIG. 3D

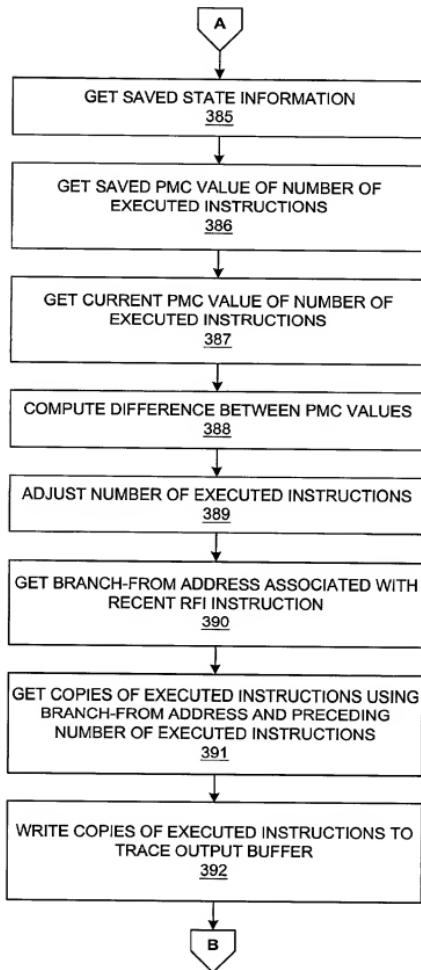


FIG. 3E